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Search Results - Record(s) 1 through 3 of 3 returned.

 1. Document ID: US 5630153 A

L2: Entry 1 of 3

File: USPT

May 13, 1997

US-PAT-NO: 5630153

DOCUMENT-IDENTIFIER: US 5630153 A

TITLE: Integrated digital signal processor/general purpose CPU with shared internal memory

L2: Entry 1 of 3

File: USPT

May 13, 1997

US-PAT-NO: 5630153

DOCUMENT-IDENTIFIER: US 5630153 A

TITLE: Integrated digital signal processor/general purpose CPU with shared internal memory

DATE-ISSUED: May 13, 1997

INT-CL: [6] G06F 9/26, G06F 9/40, G06F 9/44, G06F 13/36

US-CL-ISSUED: 395/800, 395/290, 395/306, 364/DIG.1, 364/DIG.2

US-CL-CURRENT: 712/35, 710/110, 710/126, 712/36

FIELD-OF-SEARCH: 395/800, 395/290, 395/200.01, 395/375, 395/200.03, 395/700, 395/289, 395/287, 395/297, 395/2.79, 395/183.19, 395/2.55, 395/775, 395/306, 364/DIG.1, 364/DIG.2, 364/706, 364/736, 364/726

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMM	Drawn Desc	Image
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 2. Document ID: US 4799144 A

L2: Entry 2 of 3

File: USPT

Jan 17, 1989

US-PAT-NO: 4799144

DOCUMENT-IDENTIFIER: US 4799144 A

TITLE: Multi-function communication board for expanding the versatility of a computer

L2: Entry 2 of 3

File: USPT

Jan 17, 1989

US-PAT-NO: 4799144

DOCUMENT-IDENTIFIER: US 4799144 A

TITLE: Multi-function communication board for expanding the versatility of a computer

DATE-ISSUED: January 17, 1989

INT-CL: [4] G06F 3/16

US-CL-ISSUED: 364/200, 379/284

US-CL-CURRENT: 710/2, 379/284, 379/388, 379/88.01, 379/88.07, 379/908, 704/275, 712/33, 712/38, 712/43

FIELD-OF-SEARCH: 364/2MSFile, 364/9MSFile, 381/36-40, 340/825.19, 179/6.08, 179/6.06, 179/18BC, 179/18G, 379/88, 379/89, 379/96, 379/284, 379/97

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)
[KOMC](#) [Drawn Desc](#) [Image](#)
 3. Document ID: US 4467414 A

L2: Entry 3 of 3

File: USPT

Aug 21, 1984

US-PAT-NO: 4467414

DOCUMENT-IDENTIFIER: US 4467414 A

TITLE: Cashe memory arrangement comprising a cashe buffer in combination with a pair of cache memories

L2: Entry 3 of 3

File: USPT

Aug 21, 1984

US-PAT-NO: 4467414

DOCUMENT-IDENTIFIER: US 4467414 A

TITLE: Cashe memory arrangement comprising a cashe buffer in combination with a pair of cache memories

DATE-ISSUED: August 21, 1984

INT-CL: [3] G06F 13/00

US-CL-ISSUED: 364/200

US-CL-CURRENT: 711/119

FIELD-OF-SEARCH: 364/2MSFile, 364/9MSFile

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)
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Term	Documents
"5630153".USPT.	1
5630153S	0
"4467414".USPT.	1
4467414S	0
"4799144".USPT.	1
4799144S	0
((("5630153".PN.) OR ("4467414".PN.)) OR ("4799144".PN.)).USPT.	3

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Search Results - Record(s) 1 through 10 of 10 returned.

 1. Document ID: US 6061779 A

L5: Entry 1 of 10

File: USPT

May 9, 2000

US-PAT-NO: 6061779

DOCUMENT-IDENTIFIER: US 6061779 A

TITLE: Digital signal processor having data alignment buffer for performing unaligned data accesses

[Full](#)[Title](#)[Citation](#)[Front](#)[Review](#)[Classification](#)[Date](#)[Reference](#)[EOMC](#)[Drawn Desc](#)[Image](#) 2. Document ID: US 5996059 A

L5: Entry 2 of 10

File: USPT

Nov 30, 1999

US-PAT-NO: 5996059

DOCUMENT-IDENTIFIER: US 5996059 A

TITLE: System for monitoring an execution pipeline utilizing an address pipeline in parallel with the execution pipeline

[Full](#)[Title](#)[Citation](#)[Front](#)[Review](#)[Classification](#)[Date](#)[Reference](#)[EOMC](#)[Drawn Desc](#)[Image](#) 3. Document ID: US 5954811 A

L5: Entry 3 of 10

File: USPT

Sep 21, 1999

US-PAT-NO: 5954811

DOCUMENT-IDENTIFIER: US 5954811 A

TITLE: Digital signal processor architecture

[Full](#)[Title](#)[Citation](#)[Front](#)[Review](#)[Classification](#)[Date](#)[Reference](#)[EOMC](#)[Drawn Desc](#)[Image](#) 4. Document ID: US 5930158 A

L5: Entry 4 of 10

File: USPT

Jul 27, 1999

US-PAT-NO: 5930158

DOCUMENT-IDENTIFIER: US 5930158 A

TITLE: Processor with instruction set for audio effects

[Full](#)[Title](#)[Citation](#)[Front](#)[Review](#)[Classification](#)[Date](#)[Reference](#)[EOMC](#)[Drawn Desc](#)[Image](#)

5. Document ID: US 5896543 A

L5: Entry 5 of 10

File: USPT

Apr 20, 1999

US-PAT-NO: 5896543
DOCUMENT-IDENTIFIER: US 5896543 A
TITLE: Digital signal processor architecture

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KOMC](#) [Drawn Desc](#) [Image](#) 6. Document ID: US 5768553 A

L5: Entry 6 of 10

File: USPT

Jun 16, 1998

US-PAT-NO: 5768553
DOCUMENT-IDENTIFIER: US 5768553 A
TITLE: Microprocessor using an instruction field to define DSP instructions

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KOMC](#) [Drawn Desc](#) [Image](#) 7. Document ID: US 5752073 A

L5: Entry 7 of 10

File: USPT

May 12, 1998

US-PAT-NO: 5752073
DOCUMENT-IDENTIFIER: US 5752073 A
TITLE: Digital signal processor architecture

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KOMC](#) [Drawn Desc](#) [Image](#) 8. Document ID: US 5638306 A

L5: Entry 8 of 10

File: USPT

Jun 10, 1997

US-PAT-NO: 5638306
DOCUMENT-IDENTIFIER: US 5638306 A
TITLE: Testing hooks for testing an integrated data processing system

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KOMC](#) [Drawn Desc](#) [Image](#) 9. Document ID: US 5625828 A

L5: Entry 9 of 10

File: USPT

Apr 29, 1997

US-PAT-NO: 5625828

DOCUMENT-IDENTIFIER: US 5625828 A

TITLE: Parallel operating CPU core and DSP module for executing sequence of vector DSP code instructions to generate decoded constellation points in QAM/TCM modem application

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KMD](#) [Draw Desc](#) [Image](#) 10. Document ID: US 4686622 A

L5: Entry 10 of 10

File: USPT

Aug 11, 1987

US-PAT-NO: 4686622

DOCUMENT-IDENTIFIER: US 4686622 A

TITLE: Computer system architecture using serial communication

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#)[KMD](#) [Draw Desc](#) [Image](#)[Generate Collection](#)

Term	Documents
DIGITAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	697274
DIGITALS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	206
SIGNAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2735964
SIGNALS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1340888
DSP.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	16772
DSPS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1885
GENERAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1293801
GENERALS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	120
PURPOSE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1031348
PURPOSES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	855131
((DIGITAL NEAR1 SIGNAL OR DSP) NEAR12 (GENERAL NEAR1 PURPOSE OR HOST OR MASTER OR MAIN) AND OPERAND\$ NEAR5 LOAD\$4 AND IDENTIF\$5 NEAR5 INSTRUCTION\$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	10

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SIGNAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2735964
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DSP.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	16772
DSPS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1885
GENERAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1293801
GENERALS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	120
PURPOSE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1031348
PURPOSES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	855131
((DIGITAL NEAR1 SIGNAL OR DSP) NEAR12 (GENERAL NEAR1 PURPOSE OR HOST OR MASTER OR MAIN) AND OPERAND\$ NEAR5 LOAD\$4 AND IDENTIF\$5 NEAR5 INSTRUCTION\$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	10

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[Refine Search:](#)(digital near1 signal or dsp) near12
(general near1 purpose or host or
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Search History

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USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(digital near1 signal or dsp) near12 (general near1 purpose or host or master or main) and operand\$ near5 load\$4 and identif\$5 near5 instruction\$	10	<u>L5</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(digital near1 signal or dsp) near12 (general near1 purpose or host or master or main) and operand\$ near5 load\$4	83	<u>L4</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(digital near1 signal or dsp) near12 (general near1 purpose or host or master or main)	6310	<u>L3</u>
USPT	5630153.pn. or 4467414.pn. or 4799144.pn.	3	<u>L2</u>
USPT	5630153.pn. or 4467414.pn. or 4799144.pn.	3	<u>L1</u>